

Figure 1

Full-Duplex Current Loop
(Prior Art)

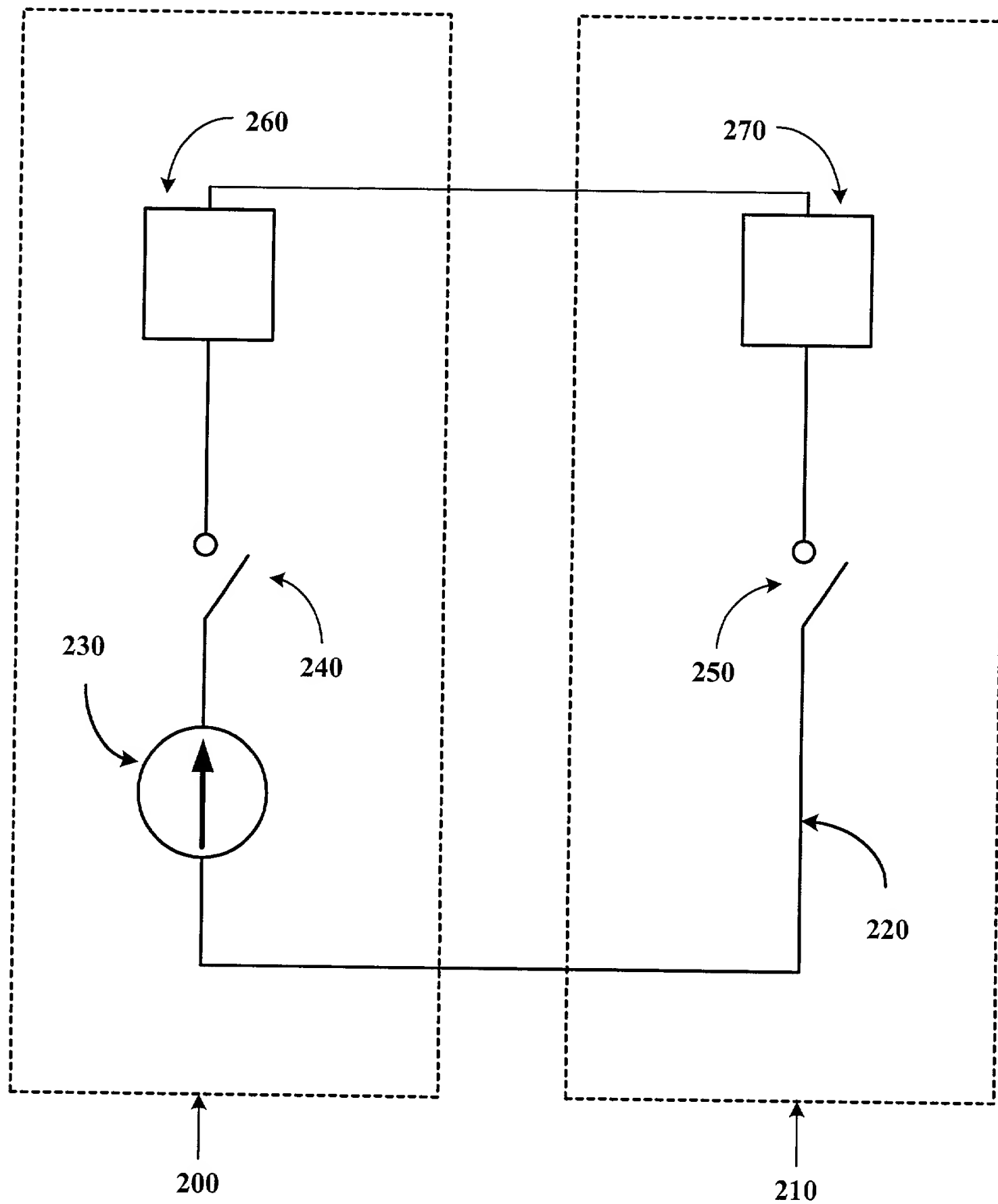


Figure 2

Simplex Current Loop
(Prior Art)

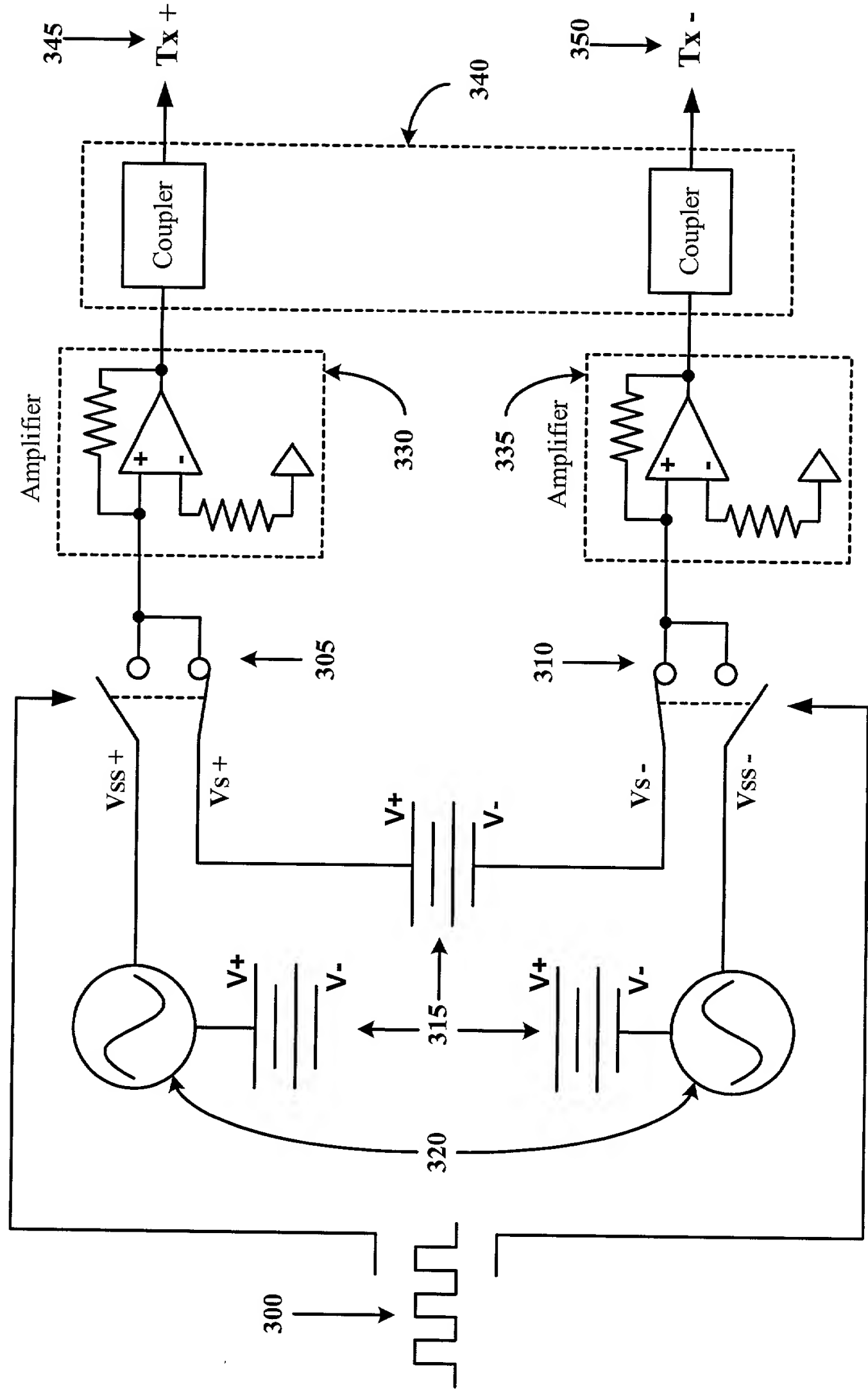


Figure 3
Transmitter

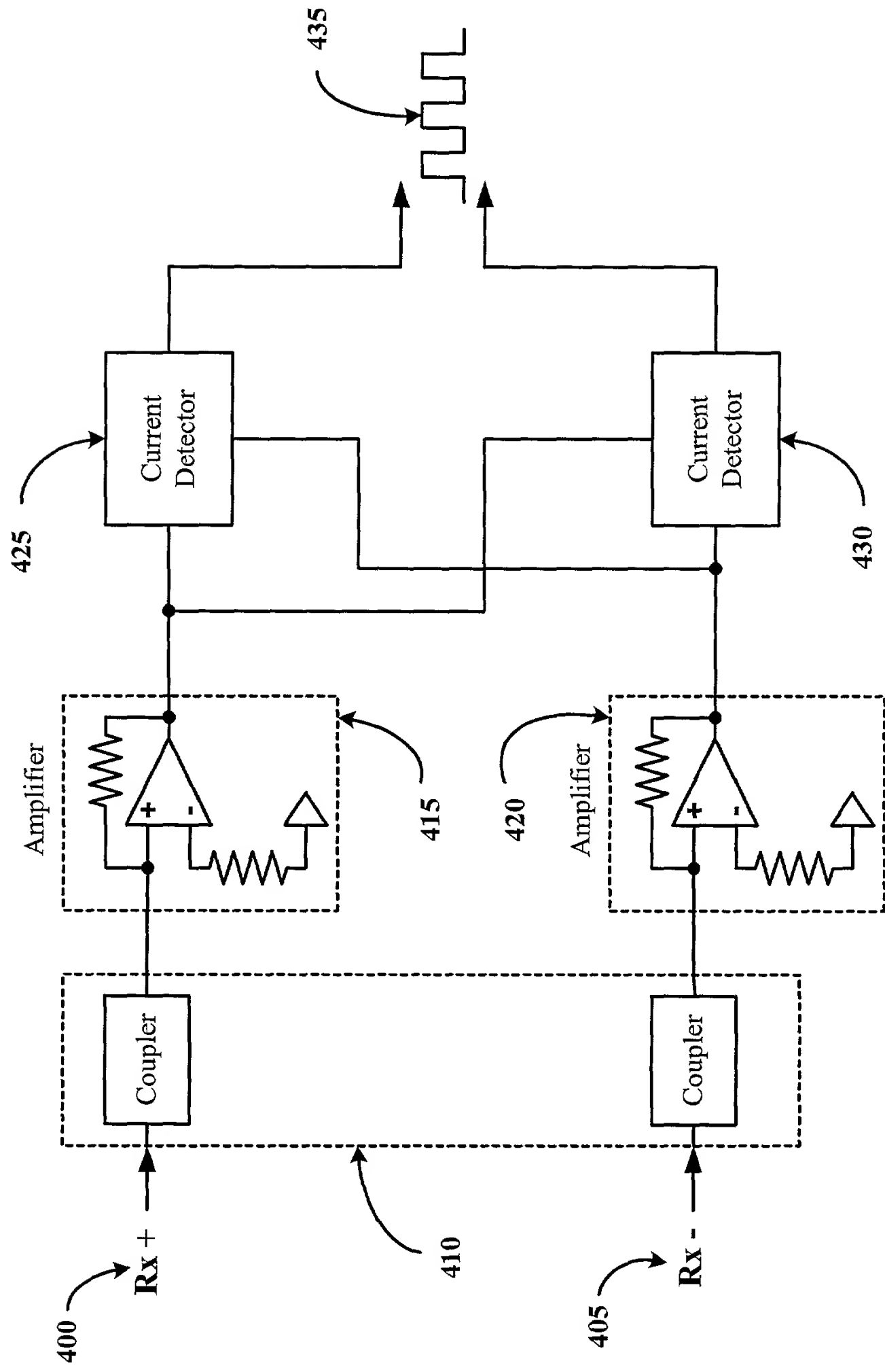
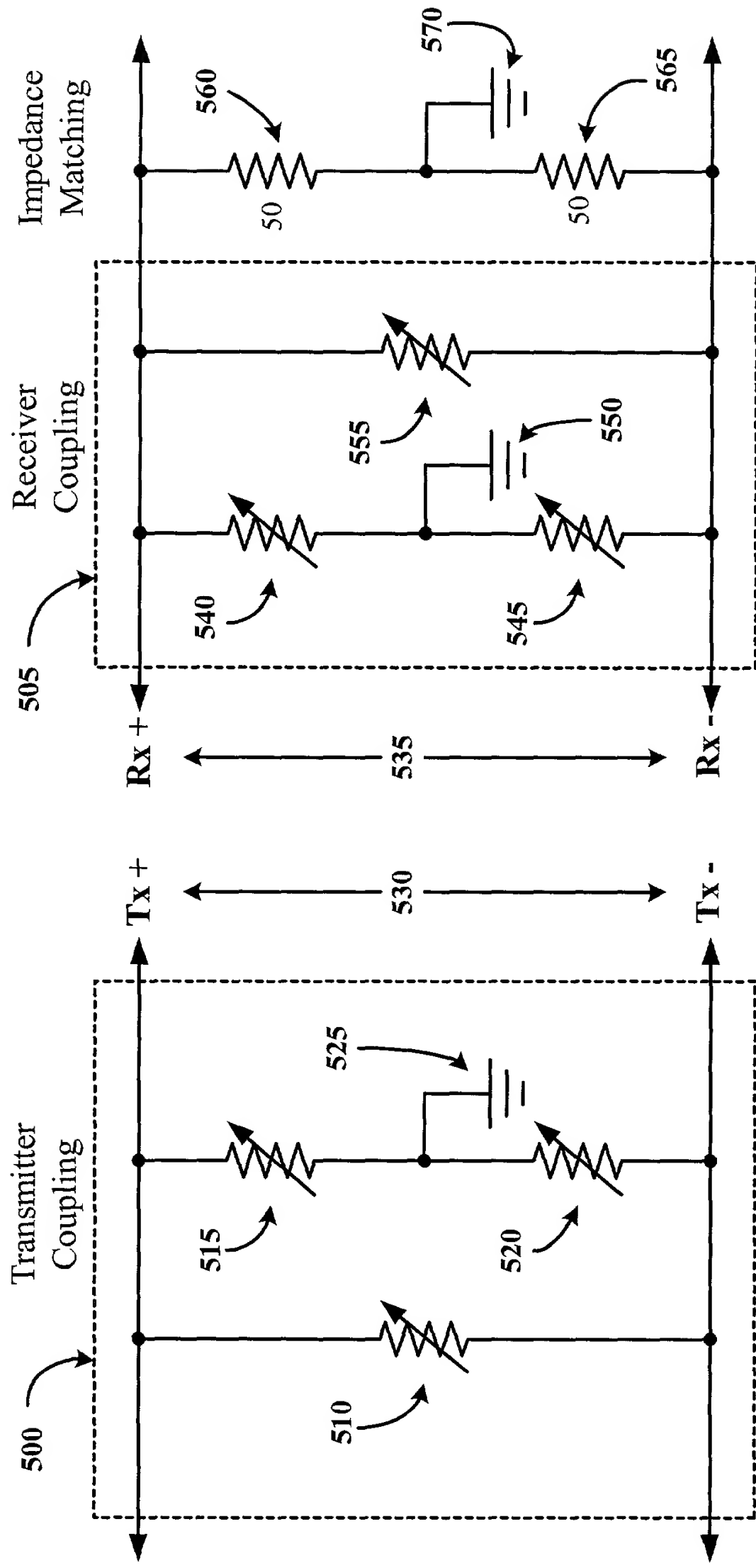


Figure 4

Receiver



Twisted Pair
Coupling

Figure 5

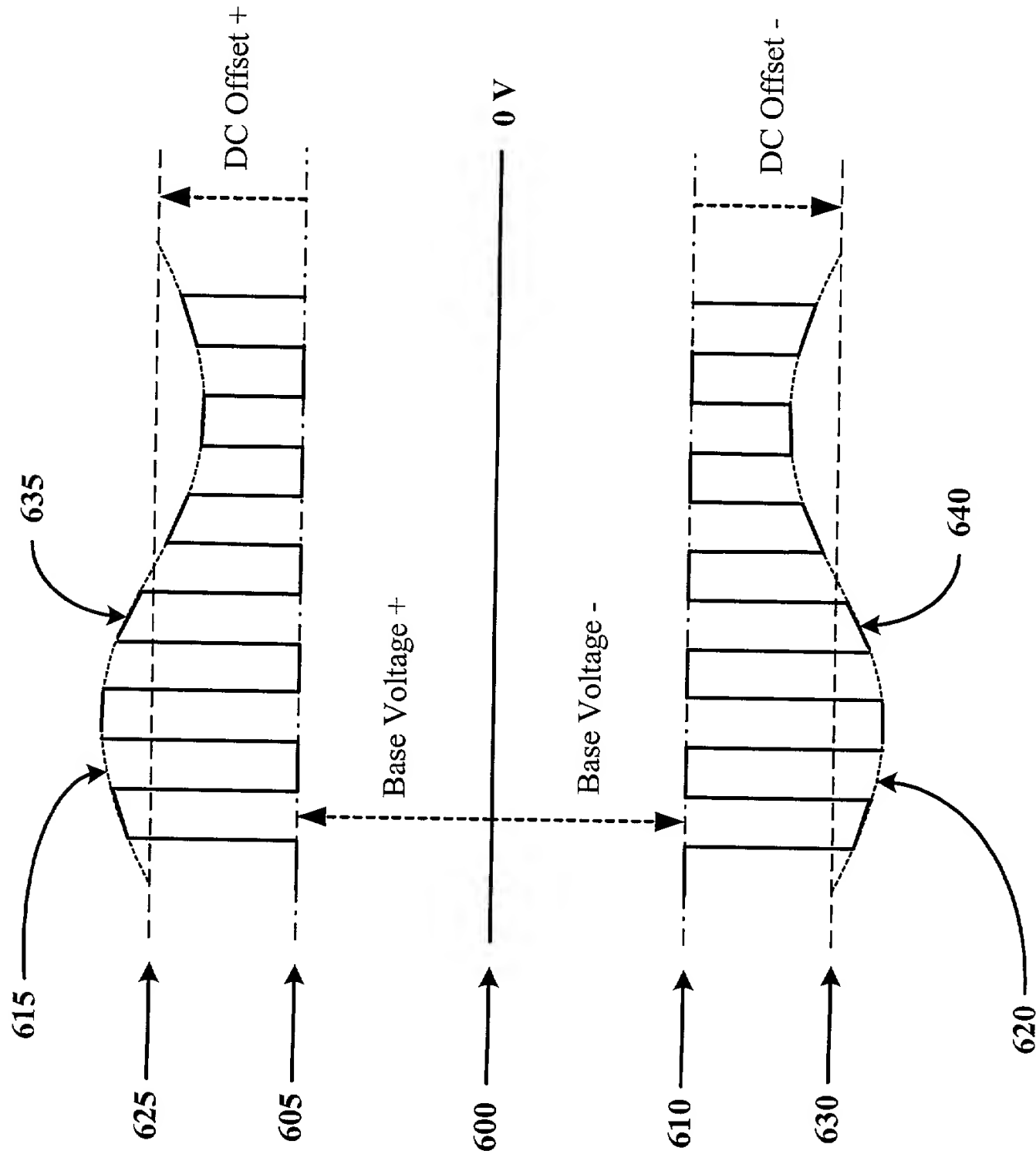


Figure 6
Transmitted Signal
With Offset Voltage

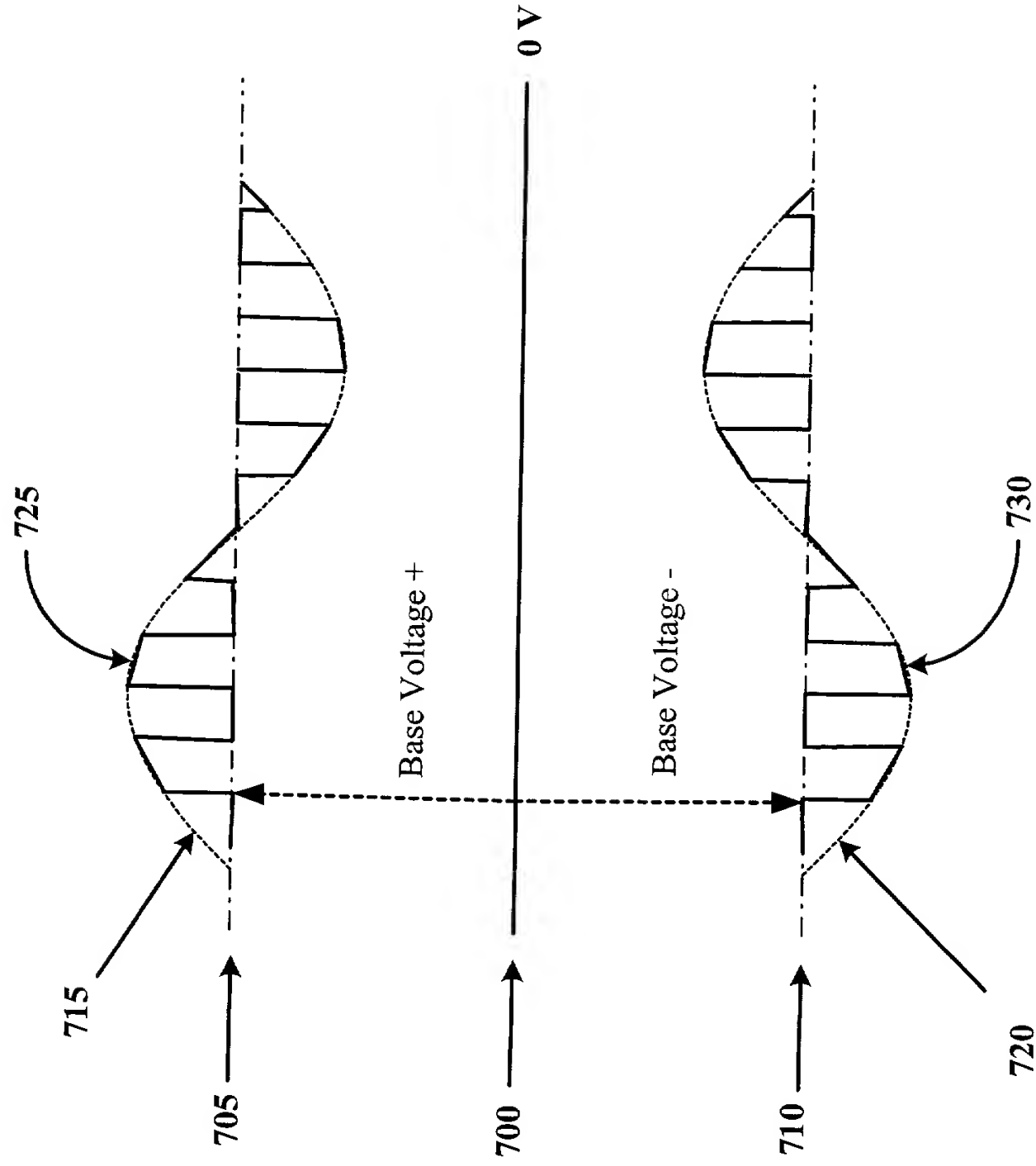


Figure 7
Transmitted Signal
Without Offset Voltage